

December 12, 2005

14:00 to 15:30

Analog Cicuits Modelling and Simulation (ACMS) I (*Mourad Loulou, Tunisia*)

Behavioral Model of Magnetic Sensors for SPICE Simulations

andrea rossini, fausto borghetti, piero malcovati, franco maloberti

VHDL-AMS modeling of a multi-standard Phase Locked Loop

B. Nicolle, L.Geynet, W. Tatinian, G. Jacquemod

Mechanical-Thermal Noise in CMOS Micromachined Inductive Microphone

B. Mezghani, F. Tounsi, S. Smaoui, M. B. Jallouli

OPTIMIZATION OF A FRACTIONAL-N FREQUENCY SYNTHESIZER WITH A VHDL-AMS DESCRIPTION

Sonia Eloued, Ahmed Fakhfakh, Nouri Masmoudi

DSP Implementation (DI) I (*Fethi Tlili, Tunisia*)

A NOVEL PIXEL TOPOLOGY FOR ON-THE-FLY PROGRAMMABLE IMAGE PROCESSING

N.Massari, M.Gottardi, A.Simoni

NOVEL HIGH THROUGHPUT IMPLEMENTATION OF SHA-256 HASH FUNCTION THROUGH PRE-COMPUTATION TECHNIQUE

H. Michail, A.Milidonis, A. Kakarountas, C. Goutis

MIGRATE LEVINSON-DURBIN BASED LINEARR PREDICTIVE CODING ALGORITHM INTO FPGAS

Jun Xu, Aladdin Ariyaeenia, Reza Sotudeh

EMBEDDED SOFTWARE IMPLEMENTATION OF AN ADAPTIVE BASE-BAND PREDISTORTER

Chiheb Rebai, Hassen Zaoui, Adel Ghazel, Haykel Ben Nasr, Slim Boumaiza, Fadhel Gannouchi

General Circuits I (*Loffi Kamoun, Tunisia*)

A SYSTOLIC TRINOMIAL GF(2^K) MULTIPLIER BASED ON THE MONTGOMERY MULTIPLICATION ALGORITHM

A. P. Fournaris, O. Koufopavlou

PARTITIONING DSP APPLICATIONS TO DIFFERENT GRANULARITY RECONFIGURABLE HARDWARE

M. D. Galanis, G. Dimitroulakos, C. E. Goutis

A NOVEL SYSTOLIC GF(2^K) FIELD MULTIPLICATION-INVERSION ARITHMETIC UNIT

A. P. Fournaris, O. Koufopavlou

Electrical Modeling of Inductive Links for High-Efficiency Energy Transmission

Mondher Chaoui, H. Ghariani, M. Lahiani, Richard Perdiau

Network and Security Engineering I (*Sami tabbane, Tunisia*)

Detection of DDOS Attacks Against Communication Systems Using the Wavelet Transform

Mohamed Hamdi, Nouha Grira, Nourddine Boudriga

CROSS-NETWORK OSA/PARLAY APPLICATIONS

Neila El Heni, Ahmed Bouabdallah, Mounir Frikha, Claude Rigault

Performance Evaluation of SCTP over E-GPRS Air Interface For HTTP Traffic

Mérim Afif, Philippe Martins, Sami Tabbane, Philippe Godlewski

The design and implementation of an XML-based configuration management architecture for IP routers

Wassef Louati, Nejat Onay Erköse, Djamel Zeghlache

VLSI Design and Implementation I (*Eric Kerherve, France*)

VLSI DESIGN AND IMPLEMENTATION OF RECONFIGURABLE CRYPTOGRAPHIC SYSTEMS FOR SYMMETRIC ENCRYPTION

G. Selimis, N. Lazarou, H.E.Michail, O. Koufopavlou

Floorplan-Aware Steiner Tree Reconstruction for Optimal Buffer Insertion

Jin-Tai Yan, Chia-Fang Lee, Tzu-Ya wang

ELECTRONICS PROPERTIES OF ANODIC OXIDIZED FILMS GROWN IN PURE WATER AT VARIOUS ANNEALING TEMPERATURE.

Bécharia NADJI

FULLY-INTEGRATED LOW-CONSUMPTION BIFET LOW-NOISE AMPLIFIER FOR WCDMA APPLICATIONS

Cristian Pavão Moreira, Eric Kerherve, Pierre Jarry, Didier Belot

Multimedia and Security (*Mohamad Al-Mualla, UAE*)

ALTERNATIVES TO VISUAL CRYPTOGRAPHY FOR COLORED IMAGES

Mahmoud Hassan

A Watermarking Technique for Geometric Manipulation Attacks

M. A. Suhail, M. S. Obaidat

HALFTONE IMAGE WATERMARKING BASED ON VISUAL CRYPTOGRAPHY

16:00 to 17:30

General Circuits II (Lotfi Kamoun, Tunisia)

Design of Direct Digital Frequency Synthesizer with High ROM Compression Ratio

Dah-Chung Chang, Li-Wen Hsu

COMPACT MATRIX INVERSION ARCHITECTURE USING A SINGLE PROCESSING ELEMENT

Fredrik Edman, Viktor Owall

CIRCUIT OPTIMIZATION BASED ON SPEED INDICATORS

AZEMARD NADINE, VERLE ALEXANDRE, LANDRAULT ALEXIS, MAURINE PHILIPPE

New Architectures For Modulo 2^n-1 Adders

Giorgos Dimitrakopoulos, Dimitrios G. Nikolos, H. T. Vergos, D. Nikolos, C. Efstathiou

Network and Security Engineering (NSE) II (sami Tabbane, Tunisia)

Improving Collaborative Jobs in P2P Networks

Adnane Cabani, Mhamed Itmi

Ordering Messages in Virtual CAN Networks

Roman Obermaisser

SDPPM: A New IP Traceback System for MANET

Zzara Faouzi, Slim Rekhis, Noureddine Boudriga, Zidane Kamel

Predictive Processing Architecture Extension for Network Processors

Juergen Foag, Thomas Wild

VLSI Design and implementation II (Adel Ghazel, Tunisia)

A New Block-XOR Precomputation-Based CAM Design for low-power embedded system

Chi-Yu Wu, Chung-Kai Cheng, Shanq-Jang Ruan

Low Power Design of Multipliers Using a Full-Adder Isolation Technique

K. Anagnostopoulos, G. Economakos

LB-Packing-Based Floorplan Design on DBL Representation

Jin-Tai Yan, Kai-Ping Lin, Yue-Fong Luo

DESIGN STRATEGY TO MINIMIZE RISE TIME AND SILICON AREA OF CHARGE PUMP WITH ONLY CAPACITIVE LOADS

Gaetano Palumbo, Domenico Pappalardo, Luigi Innacolo

Analog Cicuits Modelling and Simulation (ACMS) II (Janusz Zarebski, Poland)

SiC SCHOTTKY POWER DIODE MODELLING IN SPICE

Janusz Zarębski, Jacek D'browski

BEHAVIORAL MODEL OF CHARGE PUMPS WITH VHDL

Rosario Mita, Gaetano Palumbo, Melita Pennisi

A NOVEL MODEL FOR SINGLE PHOTON DETECTORS

Rosario Mita, Andrea Oliveri, Gaetano Palumbo, Pier Giorgio Fallica

Functional and Catastrophic Thermal Failures in Bipolar Electronic Circuits

Krzysztof Górecki, Witold J. Stepowicz, Janusz Zarębski

DSP Implementation (DI) II (Fethi Tlili, Tunisia)

Design and Implementation of a new Non-radioactive Remote Oil Tan Monitoring System

Mahmoud Meribout, AHMED AL-NAAMANY, KHAMIS AL-BUSAIDI

IMPLEMENTATION OF H.264/AVC BASELINE PROFILE DECODER FOR MOBILE VIDEO APPLICATIONS

Suh Ho Lee, Ji Hwan Park, Seon Wook Kim, Suki Kim

SPEAKER IDENTIFICATION SYSTEM BASED ON FPGA

F.A. Elmisery, H.F. Hammed, A.E. Salama, F.A. Algeldawy

XStream - A Hardware Accelerator for Digital Holographic Imaging

Thomas Lenart, Viktor Öwall

Video Coding (VC) (Mohamad Al-Mualla, UAE)

Hardware Implementation of Content Based Video Indexing Algorithms

Ben ABDELALI Abdessalem, MTIBAA Abdellatif, BOURENNANE Elbey, ABID Mohamed

AN ADAPTIVE ACTIVITY-BASED INTRA-MB REFRESH METHOD FOR ROBUST VIDEO COMMUNICATION OVER NOISY CHANNELS

Hamad Al-Hammadi, Mohammed Al-Mualla

FAST BLOCK-MATCHING MOTION ESTIMATION BASED ON AN IMPROVED CROSS-DIAMOND SEARCH ALGORITHM

HaiHua Liu, Yi Lei, ChangSheng Xie

FAST CODING OF BIDIRECTIONAL FRAME FOR H264 STANDARD

Walid Barreh, Amel Ben Azza, Fethi Tlili

December 13, 2005

09:00 to 10:30

Digital Filters and Filters Banks (DFFB) (*Hussein Al-Ahmed, UAE*)

ROOM EQUALIZATION BASED ON ITERATIVE SIMPLE COMPLEX SMOOTHING OF ACOUSTIC IMPULSE RESPONSES

Ahfir Maamar, Izzet Kale, Arthur Krukowski, Berkani Daoud

OPTIMUM BANDWIDTH IIR NOTCH FILTERS WITH MULTIPLE FREQUENCY INITIALIZATIONS

Jeedella Jeedella, Hussain Al Ahamad, Mohammed E. Al-Mualla

FEATURE EXTRACTION METHOD OF FRACTIONAL COSINE AND SINE TRANSFORM FOR SPEAKER RECOGNITION

Wang Jinfang, Wang Jinbao

Glitch reduction in digit-serial recursive filters

Krister Landernas, Johnny Holmberg

Network and Security Engineering (NSE) III (*Sihem Guemara-El Fatmi, Tunisia*)

Fast Absorb Flow Control

Abdel-Halim Smai

New correlation method for an efficient intrusion detection system

Amel Meddeb, Sihem Guemara El Fatmi, Sihem Barrah

Intrusion Tolerant GRID on Ad-Hoc Networks

Manel Abdelkader,, Nouredine Boudriga

MONITORING & MANGING DYNAMIC DISTRIBUTED SYSTEMS

Dileban Karunamoorthy, Neomal Devinuwara

Transmission Techniques (TT) I (*Sofiene Affes, Canada*)

FORWARD LINK PERFORMANCE ANALYSIS OF A DEPLOYED CDMA2000 NETWORK

Omneya Issa, Nadja Kara, Marc Courtemanche

Throughput performance of ARQ and HARQ I schemes over a two-states Markov channel model

Boujemaa Hatem

ADAPTIVE MODULATION OVER NONLINEAR TIME-VARYING CHANNELS

Hisham Al-Asady, Mohamed Ibnkahla

Performance analysis of some non-coherent space-time codes

Ines Kammoun, Antonio cipriano, Jean Claude Belfiore

VLSI Testing and Fault Tolerance I (*Malgorzata Chrzanowska-Jeske, USA*)

A Methodology for Extracting Unknown Integrated Circuit Process Parameters

Henrique Quaresma, Pedro Santos, António Serra, Etienne Sicard

Achieving Design closure in a typical Mixed-Signal ASIC; a Design-For-Test Centric Approach

Dr. Sverre Wichlund

Considering Layout for Test Scheduling of Core-based SoCs

Yu Xia, Malgorzata Chrzanowska-Jeske

Power Aware Test-Data Compression for Scan-based Testing

George Gekas, Dimitris Nikolos, Emmanouil Kalligeros, Xrysovalantis Kavousianos

Analog Circuits (AC) I (*Yann DEVAL, France*)

A STUDY ON FBAR FILTERS RECONFIGURATION

M. EL Hassan, E. Kerherve, Y.Deval, A.A. Shirakawa

Gate oxide protection in HV CMOS/DMOS integrated circuits: Design and experimental results

Robert Chebli

FULLY INTEGRATED RECTIFICATION AND BIN-INTEGRATION ANA-LOG CIRCUIT FOR BIOMEDICAL SIGNAL PROCESSING

Adnan Harb, Mohamad Sawan

Filters for VDSL Technology

Nino Stojkovic, Sinisa Jokic, Sani Basic, Marko Jusic

Methods and Tools for Circuits (*Sofiene Tahar, Canada*)

On the Formal Verification of a SystemC Packet Switch Model

Ali Habibi, Sofiene Tahar

Comparison of Methods for Predicting Distortion in Class-AB Stages

Gianluca Giustolisi, Gaetano Palumbo

A comparison between AMOLED poly-TFT driver circuits

Gaetano Palumbo, Melita Pennisi

AN INTERNET BASED GRAPHICAL PROGRAMMING TOOL FOR LEARNING POWER SYSTEM HARMONICS MEASUREMENT
Hsiung Cheng Lin

11:00 to 12:30

DSP Algorithms and Methods (DAM) (*Hussein Al-Ahmed, UAE*)

THE EVOLVING GENERATION AND FAST ALGORITHMS OF WALSH TRANSFORMS

Ningtao chen, Nengchao wang, Baochang shi

A comparative study between pre-whitened based sign algorithms in time-varying environments

Sofia Ben Jebara, Hichem Besbes

EXTENDED LAPPED TRANSFORMS WITH LINEAR PHASE BASIS FUNCTIONS AND PERFECT RECONSTRUCTION

K M Abdul Hameed, Elias Elizabeth

An Efficient Algorithm and Architecture for Natural Logarithm using Maclaurin Series

Aroutchelvame, S.M., K. Raahemifar

DSP for Industrial Process (DIP) (*Federico Baronti, Italy*)

60-channel high-resolution counter array for high-speed continuous long-term data acquisition

Federico Baronti

3D NANOVISION FOR THE INSPECTION OF MICRO-ELECTRO-MECHANICAL SYSTEMS

Chris Kammerud, Besma Abidi, Shafik Huq, Mongi Abidi

classification of tissues by neural network

Yassine Ben Salem, Salem Nasri, Rached Tourki

POLYMER-ELECTROLYTE-FILM-BASED HUMIDITY SENSOR WITH INTEGRATED SIGNAL CONDITIONER

Donato Manzan Junior, Viviane Nogueira, Marco Aurélio De Paoli, Carlos Alberto dos Reis Filho

Neural Networks (NE) (*Zied BelHaj, Tunisia*)

DESIGN AND IMPLEMENTATION OF A NEURAL COMMAND RULE ON A FPGA CIRCUIT

Ahmed Karim Ben Salem, Slim Ben Saoud

On-chip multi-layer perceptron and time-delay neural networks for phoneme recognition

Gatt Edward, Micallef Joseph, Chilton Edward

INTEGRATED RECURSIVE LEAST SQUARE LATTICE AND NEURO-FUZZY MODULES FOR MOBILE MULTI-SENSOR DATA FUSION

Mahmoud El-Gizawy, Aboelmagd Noureldin, Naser El-Sheimy

POWER SCALABLE IMPLEMENTATION OF ARTIFICIAL NEURAL NETWORKS

Sankalp Modi, Peter Wilson, Andrew Brown

Network and Security Engineering (NSE) IV (*Mohammad Obeidat, USA*)

Multicast Routing Protocol for Ad Hoc Networks with a Quality of Service ' QoS-AODV '

Ines BEN BRAHIM, Maher Ben Jemaa

Cooperative Intrusion Detection and Tolerance System

Yacine DJEMAIEL, Slim REKHIS, Noureddine BOUDRIGA

TFMCC-Based on a New Equation for Multicast Rate Control

Wafa Kammoun, Habib Youssef

Privacy Enhanced Access Control Mechanism for Home Networks

Jinbum Hwang, Jongwook Han

VLSI Testing and Fault Tolerance II (*Adel Ghazel, Tunisia*)

Dependability Computations for Fault-Tolerant System Based on FPGA

Radek Dobias, Pavel Kubalik, Hana Kubatova

INTELLIGENT SENSOR INTERFACE FOR AUTOMOTIVE APPLICATIONS

Francesco D'Ascoli, Marco Tonarelli, Adolfo Giambastiani, Luca Fanucci

Improving Effective Yield Through Error Tolerant System Design

Ahmed M. Eltawil, Fadi Kurdahi

An Embedded IDDQ Testing Circuit and Technique

Sotirios Matakias, Yiorgos Tsiatouhas, Angela Arapoyanni, Themistoklis Haniotakis

Analog Circuits II (*Fadhel Ghannouchi, Canada*)

Design of Static and Dynamic Translinear Circuits based on CMOS CCII Translinear Loop.

Debashis Dutta, Ritesh Ujjwal, Ruchi Puri, Wouter A. Serdijn

A Trimmable RC-Oscillator for Automotive Applications, with Low Process, Supply, and Temperature Sensitivity

Federico Vincis, Luca Fanucci

A Double Polarity CMOS Peak-and-Hold Circuit for Satellite Radiation Detection Systems

andrea rossini, fausto borghetti, piero malcovati, riccardo magni

AUTOMATIC TUNING CIRCUIT FOR Gm-C FILTERS

Young-Ho Kim, Hyun-Kyu Yu

14:00 to 15:30**Control and Power Systems (CPS) I (Safia Belghith; Tunisia)****A Three-Phase Single-Switch Fly-back Single-Stage Rectifier with Lossless Isolated Snubber***Huang Xiaojun, Gong Jian, Huang Jiqing***HARDWARE IMPLEMENTATION OF FUZZY LOGIC CONTROLLER***Hedi ABDELKRIM, Slim BEN SAOUD***DEVELOPMENT LANGUAGES AND ENVIRONMENTS FOR INDUCTION COOKING SYSTEM DESIGN AND SIMULATION***Alessio Beato, Massimo Conti, Claudio Turchetti***ON LINE MULTI-LOCATION POWER SYSTEM HARMONICS MEASUREMENT VIA THE INTERNET***Hsiung Cheng Lin***Data Conversion and Modulation (DCM) I (Patrick Loumeau, France)****Nonuniform Sampling Digital PLL with Fast Error Correction Technique***Abdallah Al-Zaabi, Mahmoud Al-Qutayri, Saleh Al-Araji***CURRENT MODE PIPELINED A/D CONVERTER***Krzysztof Wawryn, Robert Suszynski, Bogdan Strzeszewski***Non-Linear Behavioral Model of a Bipolar Track and Hold Amplifier For High-Speed and High-Resolution ADCs***Devrim Y. Aksin, Franco Maloberti***CALIBRATION OF SAMPLING INSTANTS IN A MULTIPLE CHANNEL TIME-INTERLEAVED ANALOG-TO-DIGITAL CONVERTERS***David Camarero, Jean-François Naviner, Patrick Loumeau***Optical Networks and Systems (ONS) I (Houria Rzig, Tunisia)****PERFORMANCE EVOLUTION FOR A NEW IMPLEMENTATION OF MULTIRATE OPTICAL FAST FREQUENCY HOPPING CDMA SYSTEM FOR MULTIMEDIA TRANSMISSION***Mourad Menif, Philippe Gallion, Chiheb Karaborni, Houria Rzig***Performance Analysis and Simulation of a Novel OBS Network Architecture***Amor Lazzez, Sihem Guemara El Fatmi, Noureddine Boudrigha***Study of Crosstalk Propagation in All-Optical Networks using Perturbation Theory***Yvan Pointurier, Maite Brandt-Pearce***Optical Label Switching: Network Design, Modeling and Performance Analysis***Yassine Khelifi, Sihem Guemara El Fatmi, Noureddine Boudrigha***RF Circuits (RC) I (Joseph Micallef , Malta)****Noise Modeling Including Effect of propagation along the gate of a field-effect transistor***M. Balti, A. Samet, D. Pasquet, E. Bourdel***The RF Circuit Design for Magnetic Power Transmission dedicated to Cochlear Prosthesis***sinda shabou, Neila rekik, Mohamed GHORBEL, Mounir SAMET***1.6-GHz Low Power Low Phase Noise Quadrature Phase Locked Loop with on chip DC-DC converter for wide tuning range***Owen Casha, Ivan Grech, Edward Gatt, Joseph Micallef***A FULLY INTEGRATED DIFFERENTIAL 5-6GHz LNA WITH ON-CHIP BALUN FOR WLAN APPLICATIONS***el gharniti ouail, eric kerhervé, jean-baptiste gueret, didier belot***VLSI Circuits (Adel Ghazel, Tunisia)****SELF-ADJUSTING MULTIPLE-PERIOD LOCKED DELAY LINE FOR HIGH RESOLUTION MULTIPHASE CLOCK GENERATION***Federico Baronti***Configurable Logic Style Translation Based on an OpenAccess Engine***Egon Valentini, Edmund Haselwanter, Robert Ulmer, Thomas Popp***An Algebraic Approach for Transistor Circuit Synthesis***Hiroaki Yoshida, Makoto Ikeda, Kunihiro Asada***A SYSTEM-LEVEL ARCHITECTURE FOR HASH MESSAGE AUTHENTICATION CODE***Hakim Khali, Riyadh Mehdi, Abdelaziz Araar***Analog Circuits (AC) III (Svante Signell, Sweden)****Filtering Transformation In Generalized Quadrature Bandpass Sampling***Yi-Ran Sun, Svante Signell***A 20 MHz BP-PWM and BP-DSM Class-D PA in 0.18 um CMOS***Johan Sommarek, Ville Saari, Jonne Lindeberg, Kari Halonen***BANDWIDTH BOOSTING IN TRANSIMPEDANCE AMPLIFIERS FOR OPTICAL LANS USING DELAYED FEEDBACK***Nuno Martins, Luis Barbosa, Luis Nero Alves, Rui L. Aguiar***A CMOS 2.45-GHZ RING OSCILLATOR WITH TEMPERATURE COMPENSATION***Vincent Cheynet de Beaupré, Wenceslas Rahajandraibe, Lakhdar Zaïd, Gilles Bas*

16:00 to 17:30

Analog and Mixed Signal processing (AMSP) (*Omar Hammami, France*)**Self-Organising Maps in Silicon for Phoneme Recognition***Gatt Edward, Micallef Joseph, Chilton Edward***A QUATERNARY LOGIC GATE USING CURRENT-MODE OPERATION WITH BIPOLAR TRANSISTORS EQUIVALENT TO AN EXCLUSIVE-OR BINARY GATE***Carlos Roberto Mingoto Junior***Digital Self-Tuning Technique for Continuous-Time Filters***Guillermo Zatorre-Navarro, Eduardo Peralias, Santiago Celma, Concepción Aldea***Dynamically Reconfigurable Analog Circuit Design Automation Through Multiobjective Optimization and Direct Execution***K.Ghali, L.Dorie, O.Hammami***VLSI Modelling, Simulation and CAD tools** (*Helena Fino, Portugal*)**PK_tool 2.0: a SystemC environment for high level power estimation***giovanni b. vece, massimo conti, simone orcioni***Techniques to Improve Cosimulation and Interoperability of Heterogeneous Models***Mathieu Dubois, El Mostapha Aboulhamid***VCOSYM - AN APPLICATION FOR THE AUTOMATIC DESIGN OF RING VCOS***Pedro Pereira, Helena Fino***Switching Activity Propagation of VHDL-RTL Combinational Designs through an Automated Tool***Felipe Machado, Teresa Riesgo, Yago Torroja***Data Conversion and Modulation (DCM) II** (*Dominique Dallet, France*)**Analysis of the ideal SFDR limit for an N bit digital-to-analog converter***Nicola Ghittori, Andrea Vigna, Piero Malcovati***DESIGN STRATEGY FOR HIGH ORDER CONTINUOUS-TIME SIGMA DELTA MODULATOR FOR MULTISTANDARD RECEIVER***Nejmeddine Jouida, Chiheb Rebai, Adel Ghazel, Dominique Dallet***HIGH ORDER CASCADE MULTIBIT ÓÄ MODULATOR FOR WIDE BANDWIDTH APPLICATIONS***S.Zouari krichen, P.Loumeau, M. Loulou, N. Masmoudi***VHDL-BASED MODELING OF A DC-DC BOOST CONVERTER***Rosario Mita, Gaetano Palumbo***Optical Networks and Systems (ONS) II** (*Houria Rzig, Tunisia*)**EXPERIMENTAL AND NUMERICAL ANALYSIS OF COUPLING LOSSES BETWEEN SINGLE MODE FIBRE AND MICROSTRUCTURE FIBRE***Rim Cherif, Mourad Zghal, Faouzi Bahloul, Pieter L. Swart***ANALYSIS OF COUPLING IN TWO DIMENSIONAL PHOTONIC CRYSTAL WAVEGUIDES***Chedlia Ben Neila, Rihab Chatta, Mourad Zghal***Study of All-Optical Wavelength Conversion based on Semiconductor Optical Amplifiers***Zied Jamai, Mourad Menif, Didier Erasme***Accurate Estimation of Q Factor for Optical Performance Monitoring Applications***Chedly B. Yahya***RF Circuits (RC) II** (*Eric Kerherve, France*)**Design and Optimization of CMOS OTA with gm/Id Methodology using EKV model for RF Frequency Synthesizer Application***Amine AYED, Hamadi GHARIANI, Mounir SAMET***LOW PHASE NOISE AND LOW POWER CMOS LC VOLTAGE CONTROLLED OSCILLATOR FOR 5-GHz WLAN APPLICATIONS***Lyes Bouzerara, Mohand tahar Belaroussi, Abdelhalim Slimane, Mohamed Boutaleb Guermaz***The using of complex mixers in order to achieve RF frequency synthesis***christophe ROUGIER, myrianne REGIS, Jean baptiste BEGUERET, Yann DEVAL***Harmonic suppression in low-pass filter designed by non uniform transmissions lines***Mohamed Boussalem, Fethi Choubani, Jacques David, Raymon Crampagne***Control and Power Systems (CPS) II** (*Reyad El-Khazali*)**A Robust Fuzzy Logic Controller With Fuzzy Adapted Scaling Factors for Vector Control of Induction Motor Drive***K. Kouzi, M-S. Naït -Saïd***Output Feedback Sliding Mode Control for Fractional Systems***Dr. Reyad El-Khazali***THERMAL SIMULATIONS OF MULTI-CHIPS MODULES***Moez Ayadi, Slim Abid, Anis Ammous***ROBUST CONTROLLER SYNTHESIS FOR THE COUPLED MASS BENCHMARK PROBLEM***amir haddadi, amin yazdanpanah*

Workshop (Modelling Computation and Services) I (*Gerard Duchamp, France*)

SEMI-AUTOMATED SOFTWARE INTEGRATION USING LOGICAL SPECIFICATIONS: AN APPLICATION TO NUMERICAL SIMULATION SOFTWARE

Mikhail KAZAKOV, Habib ABDULRAB

ONTOLOGY MEDIATORS FOR UBIQUITOUS COMPUTATIONS ENVIRONMENT

Eduard Babkin, Mikhail Zubov

TOWARDS APPLICATION OF NEURAL NETWORKS FOR OPTIMAL STRUCTURAL SYNTHESIS OF DISTRIBUTED DATABASE SYSTEMS

Edward Babkin, Margarita Karpunina

December 14, 2005

09:00 to 10:30

Computational Methods (CM) I (*Salah Ben Abdallah, Tunisia*)

New Fuzzy Ranking Algorithm for Discrete Event Simulation

Riyadh Mehdi, Abdulaziz Araar, Hakim Khali

HW/SW Design and Realization of a Size-Reconfigurable DCT Accelerator

Abdulfattah M. Obeid

LAYGEN – AN EVOLUTIONARY APPROACH TO AUTOMATIC ANALOG IC LAYOUT GENERATION

Nuno Lourenço, Nuno Horta

NUMERICAL METHOD FOR SOLVING HIGH DIMENSIONAL NON LINEAR OPTIMAL CONTROL USING THE BLOCK PULSE FUNCTIONS (BPFs)

Noureddine.Boussiala, Hafid.Chaabi

Estimation Techniques (*Baoguo Xu, China*)

Impulsive Noise Modelling Using Markov Chains in Indoor Environment – Comparison With Stochastic Model

Fatma ROUISSI, Virginie Dégardin, Adel Ghazel, Martine Liénard

Semi-blind nonstationary channel estimator based on parallel LMS filtering

Boujemâa Amara Rim

Error Performance of Narrow-Band FSK with Limiter Discriminator Detection in Nakagami-q Fading and Co-Channel Interference

Mohamed Zhaounia, Neji Youssef

TIMM Estimator versus IMM Estimator: When the First is Needed?

Mai Xu, Xiuming Shan, Baoguo Xu

Wireless Communication I (*Zied Choukair, Tunisia*)

Provision of Quality of Service in Open Wireless Architecture

Zarai Faouzi, Noureddine Boudriga

DE-Link, an antenna pointing system for stratospheric balloons

Gustav Johansson, Johan Selinder, Kalevi Hyypys

FADING CORRELATION AND ITS EFFECT ON THE SECOND ORDER STATISTICS OF MRC DIVERSITY FOR HOYT MOBILE RADIO CHANNELS

zérîbi mourad, youssef néji, choubani féthi

SIMULATION STUDY FOR ROUTING PROTOCOLS IN SENSOR NETWORKS

Abdelaziz Araar, Hakim Khali, Riyadh Mehdi

Data Conversion and Modulation (DCM) III (*Mohamed Sawan, Canada*)

Power Spectral Density Estimation of the Clock Jitter in a Continuous Time Delta Sigma Modulator with Non Return to-zero DAC Waveform

Hossein shamsi, Omid Shoaie, Roghayeh Doost

A 16-BIT SIGMA-DELTA MODULATOR FOR LOW POWER MEDICAL IMPLANTABLE SENSORS

Zhijun Lu, Benoit Gosselin, Mohamad Sawan

STF behaviour in a CT $\Sigma\Delta$ modulator

Jeroen DE MAEYER, Johan RAMAN, Pieter ROMBOUTS, Ludo WEYTEN

Transmission Techniques (TT) II (*Hatem Boujemaa, Tunisia*)

A non stationary/infinite precision system analogy for fixed point digital adaptive filter analysis

ImenMezghani Marrakchi, Mereim Jaidane, Monia Turki

Evaluation of interleaving effect on convolutional correcting code

Maher KOURAICHI, Amin TOUMI, Abdennaceur kachouri, Lotfi Kamoun

Performance of HARQ scheme using turbo codes

M. Wissem El Bahri, Fathi Raouafi, Hatem Boujemâa, Mohamed Siala

Low Complexity Frequency-Domain Turbo-Equalizer for ST-BICM over MIMO ISI channel

Sami Chtourou, Raphael Visoz, Antoine.O Berthet

Workshop (Modelling Computation and Services) II (*Cerylle Bertelle, France*)

REWRITING TABLES AND MEMORIZED SEMIRINGS

G'erard H.E. Duchamp, Khalaf Khatatneh, Cyrille Bertelle

Jebelean-Weber's Algorithm without Spurious Factors

Sidi Mohamed SEDJELMACI

EVOLUTIVE COMPLEX SCHEDULING IN INTERACTION NETWORKS FOR QUALITY IMPROVMENT IN GEOGRAPHICAL DATA BASE

UPDATING

Hakima Kadri-Dahmani, Cyrille Bertelle, Gerard H.E. Duchamp, Aomar Osmani

A SURVEY OF CHAOTIC SYNCHRONIZATION AND SECURE COMMUNICATION

Aziz Alaoui

Optical Networks and systems III (*Sihem Guemara, Tunisia*)

A VERSATILE NUMERICAL TOOL FOR THE ANALYSING AND SYSTHESING FIBER BRAGG GRATINGS

Moez Mathlouthi, Mourad Menif, Habib Hamam, Houria Rezig

A New Dynamic Resource Reservation Algorithm for OBS Networks: Design and Performance

N. Boudriga, M. S. Obaidat, M. Cherif, S. Guemara-ElFatmi

Theoretical Studies of Long Length and High Bit Rate Optical System

Sami Triki, Monia Najjar, Houria Rezig

40GHZ SHORT PULSE CLOCKED OPTICAL SOURCE BASED ON FOUR-WAVE MIXING IN AN OPTICAL FIBRE

Sondes Khemiri, Cédric Ware, Renaud Gabet, Didier Erasme

11:00 to 12:30

DSP for Radio (DR) (*Mohammed Ismail, USA*)

A Triple-Standard Transceiver Architecture for GSM, WCDMA and Wi-Fi Applications

Mohamad Ismail, Ling Zhang, Hyung Joon Kim

Design and Hardware Implementation of Digital Channel Selection decimating filter for multistandard receiver

Khaled Grati, Adel Ghazel, Lirida Naviner

A FREQUENCY PLANNING AND GENERATION SCHEME FOR MULTI-STANDARD WIRELESS TRANSCEIVERS

Mohamad Ismail, Jad G. Atallah, Wim Michielsen

System Analysis of Multiple-Input Multiple-Output WLAN Receiver

Mohamad Ismail, Chetty Garuda

Low Power and Low Voltage Circuits I (*Jean-Baptiste Begueret, France*)

A CMOS Voltage Reference for Ultra Low-Voltage Applications

Luis H. C Fereira, Tales C Pimenta

Differential Voltage Current Conveyor and Fully Differential Current Conveyor in Standard

Hesham Hamed, Ashraf Khalaf

A VERY LOW POWER 3GHZ 128/129 CMOS DUAL-MODULUS PRESCALER

Rasoul Dehghani

LOW POWER QUADRATURE GENERATION USING INJECTION-LOCKED RING OSCILLATOR DIVIDER

Rasoul Dehghani

Wireless Communication II (*Ziad Choukair, Tunisia*)

System level simulation for TD-SCDMA using smart antennas

Liu Bing, Wang Yafeng, Yang Dacheng

Research on Topology of Wireless Sensor Networks

tianhui, yangning, huangping

BER SIMULATION FOR WLAN NETWORKS IN REALISTIC ENVIRONMENT

Mohamed Ayadi, Houda Khedher, Sami Tabbane

Scalable Mobility management in Wireless Ad Hoc Sensor Systems

Mourad Baghdadi, Nouredine Boudriga

Computational Methods (CM) II (*Salah Ben Abdallah, Tunisia*)

Fuzzy Model Based Predictive Control of Nonlinear Systems

Djebrani Salima, Abdessemed Foudil

CONSTRUCTING NEIGHBOR-JOINING PHYLOGENETIC TREES WITH REDUCED REDUNDANCY COMPUTATION

Ningtao chen, Baochang shi, Nengchao wang

On a Robust PID Controller Parameters Optimization Applied to a Mass-Spring System

Boumella Nora, Boulemden Mohammed

A Hybrid Algorithm to Reconfigure Platforms of mobile radio services

asma ben letaifa, sami tabbane, zied choukair

Transmission Techniques III (*Amel Ben Azza, Tunisia*)

A Statistical Modelling of The Underwater Acoustic Channel

Ons BENRHOUMA, Sébastien HOUCKE, Ammar BOUALLEGUE

OPTIMIZATION PROCESS OF POWER DISTRIBUTION FOR A FREQUENCY MULTIPLEXING DMT TRANSMISSION

Mohamed Tlich, Jean-Claude Belfiore, Ahmed Zeddani

Craig Representation of the Product of Two Gaussian Q Functions and Its Application

Mojtaba Vaezi, Seyed Mostafa Safavi

General Circuits (GC) III (*Messaoud hassani*)

HARDWARE IMPLEMENTATIONS OF BFNN AND RBF NEURAL NETWORKS IN FPGA TECHNOLOGY

D. Sellami Masmoudi, Mohamed Krid, Mohamed Chtourou

A genetic algorithm based resources optimization methodology for implementing Artificial Neural Networks on FPGAs

Emir Damergi, Benrebaa Abdellatif, Bouallegue Ammar

OPTIMAL EXPANSION OF DISCRETE VOLTERRA MODELS USING INDEPENDENT LAGUERRE BASIS

Tarek Garna, Abdelkader Mbarek, Hassani Messaoud

NON LINEAR PREDICTIVE CONTROL ALGORITHM BASED ON LAGUERRE-VOLTERRA MODELS

Tarek GARNA, Abdelkader MBAREK, Hassani MESSAOU

14:00 to 15:30

Image and Speech Processing I (*Amel Ben Azza Ben Yahia, Tunisia*)

LINEAR SCALAR QUANTIZATION OF WAVELET IMAGE DECOMPOSITION USING JOINT OPTIMIZATION

Riadh Abdelfattah, Olivier Rioul, Pierre Duhamel

Special-Domain Speech Synthesizer

Minxin Shen, Shih-Ming Huang, Chih-Chung Kuo

lossless coding of hyperspectral images through components ordering by minimum bidirected spanning tree

Kais Siala, Amel Benazza-Benyahia

An embedded coder for multispectral image compression based on vector lifting structures

Jamel Hattay, Amel Benazza-Benyahia, Jean-Christophe Pesquet

Reception Techniques (RT) I (*Neji youssef, Tunisia*)

On the Existence of Nonlinear Equalizer

Hichem Arfa, Sadok El Asmi, Safya Belghith

Joint Angle and Delay estimation of point sources

Insaf Jaafar, Rim Amara, Hatem Boujemâa, Mohamed Siala

Maximum a posteriori discrete-time rake receiver for diffuse multipath channels

Mohamed Siala

On the performance of the decorrelating and the conventional discrete time rake receivers with improved channel estimation

Imed HADJ KACEM, Hatem BOUJEMAA, Mohamed Siala

RF Systems (RS) (*Ammar Kouki, Canada*)

OPTIMIZED MULTISTANDARD RF SUBSAMPLING RADIO RECEIVER DESIGN

Rim BARRAK, Adel Ghazel, Fadhel Ghannouchi

MULTI-CORRELATOR BASED MULTIPATH MITIGATION TECHNIQUE FOR A GPS RECEIVER

Chiheb Rebai, Fahmi Missaoui, Adel Ghazel

FPGA BUILDING BLOCKS FOR AN HYBRID BASE BAND DIGITAL PREDISTORTER SUITABLE FOR 3G POWER AMPLIFIERS

Chiheb Rebai, Salman Labiadh, Khaled Grati, Adel Ghazel, Slim Boumaiza, Fadhel Ghanouchi

TEMPERATURE COMPENSATED DIGITAL PREDISTORTER FOR 3G POWER AMPLIFIERS

Oualid Hammi, Fadel Ghannouchi

Low Power and Low Voltage Circuits II (*Ismail Nabhan, Taiwan*)

Design of a low-power variable gain amplifier for reconfigurable wireless receivers

Nicola Ghittori, Andrea Vigna, Piero Malcovati, Stefano D'Amico, Andrea Baschirotto

Analog VLSI CMOS Implementation of Beta Neuron in

Dorra Sellami Masmoudi, Mohamed Masmoudi, Adel Alimi

A NOVEL LOW POWER HIGH DYNAMIC RANGE ALL CMOS CURRENT-MODE AGC

Chung-Yu Wu, Ismail I. Nabhan

Design And Implementation of a Low Power MEMS Based Reconfigurable VCO

H. P. Le, J. Singh

Wireless Communication III (*Noureddine Boudriga, Tunisia*)

Simulation of Mobile-to-Mobile Radio Fading Channels

nazih hajri, neji youssef

Adaptive Routing Considering The State of Receiver for Ad Hoc Networks

tianhui, yangning

End-to-End QoS Management and Real Time Agreement Protocol for Resource Reservation for Multimedia Mobile Radio Network

Sonia Ben Rejeb, Zied Choukair, Sami Tabbane

INDOOR PREDICTION OF PROPAGATION USING DOMINANT PATH: STUDY AND CALIBRATION

Mohamed Bechir Khrouf, Mohamed Ayadi, Sami tabbane, Ziad Belhadj

VLSI Design and Implementation III (*Adel Ghazel, Tunisia*)

A Multiprocessor Architecture for Fast Packet Processing

A. RAMAZANI, F. MONTEIRO, C. DIOU, A. DANDACHE

Pipeline Synthesis of SRSI Circuits

Rashad Oreifej, Abdelhalim Alsharqawi, Abdel Ejnoui

On-chip Non-Periodic High-Swing Noise Detector

Mohamed Abbas, Makoto Ikeda, Kunihiro Asada

16:00 to 17:30

Cellular Networks (CN) (Sami Tabbane, Tunisia)

USE OF DISTRIBUTED ANTENNA SYSTEMS IN MAXIMIZING DATA THROUGHPUT IN 3G MICROCELLULAR SYSTEMS- SIMULATION STUDY OF THE EFFECT OF CORRELATED SHADOWING

Elyes Ben Ali Bdira

Mobility and Traffic Models for Reconfigurable Networks of 3rd and 4th Generations

asma ben letaifa, Houda Khedher, Bassem Boussetta, sami Tabbane

DOWNLINK CAPACITY ESTIMATION FOR UMTS NETWORK : IMPACT OF USERS' POSITION, SERVICE BIT RATES AND CELL RADIUS

Soumaya Hamouda, Philippe Godlewski, Sami Tabbane

UMTS Integration with Satellite Component Design, Protocol Architecture and Performance Analysis.

nawel zangar, marwen abdennebi, samir tohme, sami tabbane

Orthogonal Frequency Division Multiplexing (OFDM) (Soufia Ben Jebara, Tunisia)

Improved estimation of time varying and frequency selective channels for OFDM systems

El Kefi Hlel, Sofiane Cherif, Fethi Tlili, Mohamed Siala

Performance comparison of OFDM and OFDM/OQAM systems operating in highly time and frequency dispersive radio-mobile channels

Amin Ben Salem, Mohamed Siala, Hatem Boujemaa

PILOT-AIDED ERASURE TECHNIQUE TO COMBAT IMPULSIVE NOISE IN OFDM POWER LINE COMMUNICATION SYSTEM

Yosra MLAYEH, Fatma ROUISSI, Fethi TLILI, Adel GHAZEL

A Unified Framework for the Construction of OFDM/OQAM Systems

Mohamed Siala, Tolga Kurt, Abbas Yongaçoglu

Optimization of the OFDM prototype waveform for highly time and frequency dispersive channels through a maximization of the SIR

Naoufel Debbabi, Mohamed Siala, Hatem Boujemaa

Low Power and Low Voltage Circuits III (Sofiène Tahar, Canada)

Analysis and Optimization of a Low-Voltage Class-AB Output Stage

Walter Aloisi, Gianluca Giustolisi, Gaetano Palumbo

MODELING POWER BUDGET REQUIREMENTS OF IMPLANTABLE ELECTRONIC DEVICES

Saeid Hashemi, MOhamad Sawan, Yvon Savaria

Design of ultra low-power analog cells with dynamic current biasing

Ivano Galdi, Paolo Boi, Tianhon Yang, Franco Maloberti

An Improved Low Voltage CMOS Current Source Exploiting Positive Shunt Feedback

Arturo Tauro, Marco Di Ciano, Cristoforo Marzocca

Reception Techniques (RT) II (Hichem Besbes, Tunisia)

Maximum a posteriori detection of the primary synchronization channel of the UMTS/FDD mode

Mohamed Siala, Sofiène Affès

FSK Zero-Crossing Demodulator for 802.15.4 Low-cost receivers

Nicolas Dehaese, Sylvain Bourdel, Herve Barthélemy, Yannick Bachelet

Design of a QAM Receiver with the Kalman Algorithm for Adaptive Carrier Synchronization

Dah-Chung Chang, Wei-Tsen Lin

HETEROGENEOUS IMPLEMENTATION OF A RAKE RECEIVER FOR DS-CDMA COMMUNICATION SYSTEMS

Daniel Souza, Ioannis Krikidis, Lirida Naviner, Jean-Luc Danger

Image and Speech Processing (Zied Belhadj, Tunisia)

Detection and Extraction of the Text in a video sequence

Hichem KARRAY, Adel ALIMI

DSM Extraction From SPOT5 stereo-pairs

Yosra Kassis, Riadh Tebourbi, Belhadj Zied, Michel Roux

A TEXTURE BASED MULTISPECTRAL IMAGES INDEXING

Tebourbi Riadh, Belhadj Zied

A "Real-like" polarimetric weather radar data generation using physical and statistical models

Amor Elmzoughi, Veronica Santalla del Rio, Riadh Abdelfatteh, Zied Belhadj

Data conversion and Modulation IV (Adel Ghazel, Tunisia)

Sigma Delta A/D Converter Architecture

Nizar Khitouni, Sonia Boujelben, Mohamed Masmoudi

A Charge Redistribution SAR ADC

sabrina meguellati, Nour eddine bouguechal, manck otto, ruediger arnold

FULLY DIFFERENTIAL GAIN-BOOSTED OTA DESIGN FOR PIPELINE DA-CONVERTER IN 180NM CMOS TECHNOLOGY

Mezbah Uddin Shaber, Svante Signell

5. Symbolic Analysis of Analog Circuits by Flow-Graphs Mourad Fakhfakh, Irina Asenova and Mourad Loulou. 6. Analysis and Synthesis of Electronic Circuits by Two-Graph Method Marian Pierzchala and Benedykt Rodanski. About the Editors Mourad Fakhfakh got the engineering and the PhD degrees from the national engineering school of Sfax Tunisia in 1996 and 2006 respectively. From 1998 to 2004 he worked in the Tunisian National Company of Electricity and Gas (STEG) as the head of the technical intervention department. It is an essential complement to numerical simulation. Research on symbolic analysis can be dated back to 19th century. Electronic circuit simulation uses mathematical models to replicate the behavior of an actual electronic device or circuit. Simulation software allows for modeling of circuit operation and is an invaluable analysis tool. Due to its highly accurate modeling capability, many colleges and universities use this type of software for the teaching of electronics technician and electronics engineering programs. Electronics simulation software engages its users by integrating them into the learning experience... Sallem, Amin and Mourad Fakhfakh, Esteban Tlelo-Cuautle, and Mourad Loulou. "SODAC: A Simulation-Based Tool for the Optimal Design of Analog Circuits," International Journal of Applied Metaheuristic Computing (IJAMC) 3 (2012): 4, accessed (April 13, 2021), doi:10.4018/jamc.2012100104. Export Reference. Available In. Analog circuit design and sizing is a very complicated and iterative process. Unlike its digital counterpart, it has not yet reached a mature stage. This is due to many reasons, mainly because the analog design field presents an extra large spectrum of circuit performances, constraints and tradeoffs. Comparisons with other approaches are given, and simulation results are presented to further highlight these statements. PartSim is a free and easy to use circuit simulator that includes a full SPICE simulation engine, web-based schematic capture tool, a graphical waveform viewer that runs in your web browser. Update export option value for ansi b page format. Added keyboard shortcuts dialog. Added more simulation examples. Update properties sidebar with the grid type option. Update properties sidebar for page format and viewport options. Release 2.0.6. August 09, 2017.