



<p>An Algorithm-Based Recovery Scheme for Exascale Computing Hui Liu (Colorado School of Mines, Golden, US)</p>
<p>Lightweight Methods for Automated Design of Self-Stabilization Aly Farahat and Ali Ebnenasir (Michigan Technological University, US)</p>
<p>A Parallel Time-dependent Multimodal Shortest Path Algorithm Based on Geographical Partitioning Hedi Ayed (CRP Henri Tudor Luxembourg, Luxembourg, LU)</p>
<p>Communication Optimization Beyond MPI Andrew Friedley and Andrew Lumsdaine (Indiana University, Bloomington, US)</p>
<p>Efficient Agreement Protocols in Asynchronous Distributed Systems Izabela Moise (University Rennes 1, IRISA/INRIA, Rennes, FR)</p>
<p>Efficient Verification Solutions for Message Passing Systems Subodh Sharma and Ganesh Gopalakrishnan (University of Utah, Salt Lake City, US)</p>
<p>Decentralized Network Bandwidth Prediction and Node Search Sukhyun Song (University of Maryland, College Park, US)</p>
<p>Large-Scale Parallel Monte Carlo Tree Search on GPU Kamil Rocki and Reiji Suda (The University of Tokyo, JP)</p>
<p>Memory-aware algorithms and scheduling techniques: from multicore processors to petascale supercomputers Mathias Jacquelin (ENS Lyon, FR)</p>
<p>Memory Hierarchy Aware Parallel Priority Based Data Structures Dinesh Agarwal and Sushil Prasad (Georgia State University, Atlanta, US)</p>
<p>Parallel Algorithms for Bayesian Networks Structure Learning with Applications to Systems Biology Olga Nikolova and Srinivas Aluru (Iowa State University, Ames, US)</p>
<p>Fault tolerant data acquisition through dynamic load balancing Michał Simon (Silesian University of Technology, Geneva, CH)</p>
<p>A Codesigned Fault Tolerance System for Heterogeneous Many-Core Processors Keun Soo Yim and Ravishankar Iyer (University of Illinois at Urbana-Champaign, US)</p>
<p>Towards a storage backend optimized for atomic MPI-IO for parallel scientific applications Viet-Trung Tran (ENS Cachan, IRISA/INRIA, Rennes, FR)</p>
<p>Programming Heterogeneous Systems David Kunzman and Laxmikant Kale (University of Illinois at Urbana-Champaign, Urbana, US)</p>
<p>Data Parallel Programming Model for Many-Core Architectures Yongpeng Zhang (North Carolina State University, Dickson, US)</p>
<p>Detection and Correction of Silent Data Corruption for Large-Scale High-Performance Computing David Fiala (North Carolina State University, Dickson, US)</p>
<p>Improving Job Scheduling on Production Supercomputers Wei Tang and Zhiling Lan (Illinois Institute of Technology, Chicago, US) and Narayan Desai (Argonne National Laboratory, US)</p>
<p>Towards a Self-Adaptive Data Management System for Cloud Environments Alexandra Carpen-Amarie (INRIA/IRISA, Rennes, FR)</p>
<p>An Integrated Scratch Management Service for HPC Centers Henry Monti (Virginia Tech, US)</p>
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<p>Automatic Generation of Executable Communication Specifications from Parallel Applications Xing Wu and Frank Mueller (North Carolina State University, Dickson, US) and Scott Pakin (Los Alamos National Laboratory, US)</p>
<p>Scout: High-Performance Heterogeneous Computing Made Simple James Jablin (Brown University, Providence, US) and Pat McCormick (Los Alamos National Laboratory, US) and Maurice Herlihy (Brown University, Providence, US)</p>
<p>Building Dynamic Computing Infrastructures over Distributed Clouds Pierre Riteau (University Rennes 1, IRISA/INRIA, Rennes, FR)</p>
<p>Next Generation Sequencing: Algorithms and Applications Xiao Yang (Iowa State University, Ames, US)</p>
<p>Performance Analysis of Long-running Applications Zoltan Szebenyi (Jülich Supercomputing Centre, DE) and Felix Wolf (German Research School for Simulation Sciences, Aachen, DE) and Brian Wylie (Jülich Supercomputing Centre, DE)</p>
<p>p2MATLAB: Productive Parallel MATLAB for the Exascale Vipin Sachdeva and David Bader (Georgia Institute of Technology, Atlanta, US)</p>
<p>A Framework for Automated Performance Tuning and Code Verification on GPU Computing Platforms Allison Gehrke and Daniel Connors and Ilkyeun Ra (University of Colorado Denver, Denver, US)</p>

(*Note: Author listed first is PhD student who will be presenting poster.)

20th IEEE International Parallel & Distributed Processing Symposium April 25-29, 2006 Rhodes Island, Greece. Welcome. Getting Started.Â Session 19: Fault and Failure Tolerance. Session 20: MPI. Session 21: Routing. Session 22: Image Processing and Visualization. Session 23: Reconfigurable and Multiple-Width Systems. Session 24: Programming Abstractions. Session 25: Resource Allocation. Session 26: Partitioning and Refinement. Session 27: Collective Communication. Session 28: Distributed Coordination. Session 29: Symbolic Computing Applications. Session 30: Multithreading. Session 31: Runtime Optimizations. Session 32: Distributed Systems. CD-ROM produced by X-CD Technologies Inc. In 20th International Parallel and Distributed Processing Symposium (IPDPS), 2006. Google Scholar. Mellanox Technologies.Â Layered Shortest Path (LASH) Routing in Irregular System Area Networks. In IPDPS '02: Proceedings of the 16th International Parallel and Distributed Processing Symposium, Washington, DC, USA, 2002. IEEE Computer Society Press. Google Scholar. IPDPS 2011 Anchorage (Alaska) USA 16-20 May 2011 www.ipdps.org. * Sponsored by IEEE Computer Society Technical Committee on Parallel Processing * In cooperation with ACM SIGARCH, IEEE Computer Society Technical Committee on Computer Architecture, and IEEE Computer Society Technical Committee on Distributed Processing. Abstracts due...24 September 2010 Papers due...1 October 2010.Â IPDPS serves as a forum for engineers and scientists from around the world to present their latest research findings in the fields of parallel processing and distributed computing. The five-day program will follow the usual format of contributed papers, invited speakers, and panels mid week, framed by workshops held on the first and last days. IPDPS (Conference) Anchorage, Alaska), IEEE XPlore Conference Proceedings Online. Format: Online Conference Proceeding Book.Â Ninth IEEE International Symposium on Parallel and Distributed Processing with Applications Workshops ISPAW 2011, 26-28 May 2011, Busan, Korea / Published: (2011). 2008 IEEE International Symposium on Parallel & Distributed Processing Miami, FL, 14-18 April 2008. Published: (2008). 800 Lancaster Ave., Villanova, PA 19085 610.519.4500 Contact.